

AMENDMENTS TO THE CLAIMS:

This listing of claims will replace all prior versions and listings of claims in the above-referenced application.

Listing of Claims:

1. (Currently amended) An LCD control unit for driving an LCD panel in an LCD device, said

LCD control unit comprising:

a signal controller for generating a voltage address signal and a polarity control signal;

a voltage generator block for generating a plurality of (n) γ -voltage levels and a plurality of (m) Vcom-voltage levels based on said voltage address signal,

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a voltage selecting block for selecting a specified number of said γ -voltage levels and one of said Vcom-voltage levels based on said polarity control signal to output said specified number of γ -correction voltages and a Vcom voltage, wherein said voltage selecting block includes an impedance converter that converts internal impedances of the γ -voltage levels and the Vcom-voltage levels and generates said specified number of said γ -correction voltages and said Vcom voltage; and

an LCD driver for generating a set of display data signals based on a set of external data signals, wherein said LCD driver receives said specified number of said γ -correction voltages output from said voltage selecting block and including includes a γ -correction section for correcting voltages of said display data signals based on said specified number of said γ -correction voltages.

2. (Original) The LCD control unit as defined in claim 1, wherein said voltage address signal and said polarity control signal are generated based on a software as time series signals.

3. (Currently amended) The LCD control unit as defined in claim 1, wherein said voltage generator block includes a resistor string for generating $n \times L$ voltage levels, n first decoders for selecting said n γ -voltage levels from said $n \times L$ voltage levels based on said voltage address signal, and m second decoders for selecting said m Vcom-voltage levels from said $n \times L$ voltage levels based on said voltage address signal, given number L being an integer.

AB 4. (Original) The LCD control unit as defined in claim 1, wherein said specified number of γ -correction voltages are a pair of γ -correction voltages.

5. (Original) The LCD control unit as defined in claim 4, wherein said voltage selecting block alternately selects said pair of γ -correction voltages having a positive polarity and said pair of γ -correction voltages having a negative polarity, with respect to said Vcom voltages.

6. (Original) The LCD control unit as defined in claim 1, wherein said voltage generator block includes a resistor string for generating a plurality of voltage levels, a decoder for decoding said voltage address signal, and a selector for selecting one of said γ -voltage levels or one of said Vcom voltage levels.

7. (Original) The LCD control unit as defined in claim 1, wherein said LCD control unit is a one-chip IC.

8. (New) An LCD control unit, comprising:

an LCD driver that drives an LCD panel in an LCD device, wherein said LCD driver includes:

a γ -correction resistor string that generates a plurality of γ -voltage levels by dividing a voltage between a first potential and a second potential; and

a voltage generator block that selects one of said plurality of γ -voltage levels based on an image data signal to deliver a display data signal, wherein said first potential and said second potential are variable.

9. (New) An LCD control unit for driving an LCD panel in an LCD device, comprising:

an LCD driver that drives said LCD panel based on a plurality of γ -correction voltages; and

a γ -voltage generator block for supplying said γ -correction voltages, said γ -voltage generator block including:

a resistor string block including a plurality of resistor groups; and

a plurality of converters each corresponding to one of said plurality of resistor groups, wherein each of said converters responds to a voltage address signal to select and deliver at least one output voltage of a corresponding one of said resistor groups.

10. (New) The LCD control unit as recited in claim 9, wherein said γ -voltage generator block includes a plurality of data latches each latching said voltage address signal in synchrony with a corresponding one of γ -clock signals, wherein each of said converters selects one of a plurality of output voltages supplied from a corresponding one of said resistor groups in response to an output from a corresponding one of said data latches.

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11. (New) The LCD control unit as recited in claim 10, further comprising a signal controller that generates said voltage address signal, said signal controller including a register for storing a suitable value for said voltage address signal depending on said LCD panel.
